

Title	Diagnose and repair a faulty microprocessor or microcontroller system		
Level	4	Credits	6

Purpose	<p>This unit standard covers diagnosis and repair of faults in microprocessor or microcontroller systems to component level for electronics technicians.</p> <p>People credited with this unit standard are able to:</p> <ul style="list-style-type: none"> – diagnose a faulty microprocessor or microcontroller system; and – repair, test, and restore microprocessor or microcontroller equipment to service.
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Classification	Electronic Engineering > Electronic Installation and Maintenance
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Available grade	Achieved
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Entry information	
Recommended skills and knowledge	It is recommended that this unit standard be assessed after competency in Unit 20432, <i>Demonstrate and apply fundamental knowledge of digital electronics for electronics technicians</i> , and Unit 20714, <i>Demonstrate and apply knowledge of digital principles for electronics technicians</i> , have been achieved, or demonstrate equivalent knowledge and skills.

Explanatory notes

- 1 This unit standard has been developed for learning and assessment off-job and is suitable for use in conjunction with computer assisted learning equipment.
- 2 Definitions
Enterprise practice – those practices and procedures that have been promulgated by the company or enterprise for use by their employees.
Industry practice – those practices that competent practitioners within the industry recognise as current industry best practice.

3 Range

- a Competence may be demonstrated on one type of microprocessor or microcontroller system.
- b The type of diagnosis and repair work must include:
 - i diagnosis and repair of equipment to component level;
 - ii use of test instruments to identify faults, measure and adjust equipment, and confirm proper performance. Typical instruments include multimeters, oscilloscopes, logic pulsers, signal tracers, and logic probes. Logic analysers or signature analysers may also be used.
- c All diagnostic and repair work must be completed within an *industry acceptable timeframe*, ie the length of time within which a competent person at this level could reasonably be expected to perform the task. In the electronics service industry, time is a significant factor in judging competence. Assessors must therefore ensure that the time taken is representative of industry expectations for the type of work.
- d Candidates are expected to meet the outcomes of this unit standard without direct technical supervision, and with complete responsibility for quality and quantity of output.
- e Electrical, radiation, and workshop or laboratory safety practices are to be observed at all times.
- f All activities and evidence presented for all outcomes and evidence requirements in this unit standard must be in accordance with legislation, policies, procedures, ethical codes, Standards, applicable site and enterprise practice, and industry practice; and, where appropriate, manufacturers' instructions, specifications, and data sheets.
- g Evidence is required of a minimum of five faults each from a different microprocessor functional area, which are to include: microprocessor timing and control signal circuitry, data transfer circuitry, address circuitry, interface circuitry, input/output devices, software, peripherals, power supplies. Fault symptoms to include incorrect or no display, incorrect or no response to keyboard, incorrect system outputs, no response or communication with peripheral devices, and incorrect or no response to external service requests.

Outcomes and evidence requirements

Outcome 1

Diagnose a faulty microprocessor or microcontroller system.

Evidence requirements

- 1.1 Fault symptoms and/or substandard performance are identified.

1.2 Diagnosis identifies the faulty sub-system.

Range: for a system crash – visual inspection; observation of key indicators; measurement of power supplies; initialisation of the central processing unit (CPU); measurement of external CPU signals; monitoring of CPU control signals, data transfer control signals, or handshake signals; additionally for a partially operational system with displays available – retention of static data and settings by re-powering; testing of display and keyboard functions; use of built in test equipment (BITE).

1.3 Diagnosis identifies the faulty component using efficient diagnostic techniques, tools, instruments, BITE where available, and servicing data.

1.4 All observations, test results, and conclusions are recorded.

1.5 Components are protected from static damage by following anti-static precautions.

1.6 Diagnostic processes do not damage the equipment.

Outcome 2

Repair, test, and restore microprocessor or microcontroller equipment to service.

Evidence requirements

2.1 Replacement or repair of faulty units or components is in accordance with servicing data and enterprise practice.

2.2 Replacement or repair does not damage other components.

2.3 Testing confirms that the microprocessor or microcontroller equipment has been restored to proper operation.

Planned review date	31 December 2016
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Status information and last date for assessment for superseded versions

Process	Version	Date	Last Date for Assessment
Registration	1	29 October 1996	31 December 2011
Revision	2	3 April 2001	31 December 2011
Review	3	26 July 2004	31 December 2012
Review	4	21 July 2011	N/A

Consent and Moderation Requirements (CMR) reference	0003
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This CMR can be accessed at <http://www.nzqa.govt.nz/framework/search/index.do>.

Please note

Providers must be granted consent to assess against standards (accredited) by NZQA, before they can report credits from assessment against unit standards or deliver courses of study leading to that assessment.

Industry Training Organisations must be granted consent to assess against standards by NZQA before they can register credits from assessment against unit standards.

Providers and Industry Training Organisations, which have been granted consent and which are assessing against unit standards must engage with the moderation system that applies to those standards.

Requirements for consent to assess and an outline of the moderation system that applies to this standard are outlined in the Consent and Moderation Requirements (CMRs). The CMR also includes useful information about special requirements for organisations wishing to develop education and training programmes, such as minimum qualifications for tutors and assessors, and special resource requirements.

Comments on this unit standard

Please contact the ElectroTechnology Industry Training Organisation reviewcomments@etito.co.nz if you wish to suggest changes to the content of this unit standard.